

## Silicon P-Channel Power MOSFET

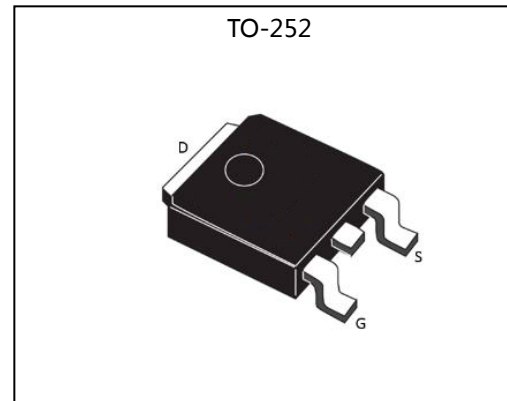
### General Description :

The HMR65P06 uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications. The package form is TO-252, which accords with the RoHS standard.

$V_{DSS}$	-60	V
$I_D$	-65	A
$P_D$	130	W
$R_{DS(ON)}$	13	m $\Omega$

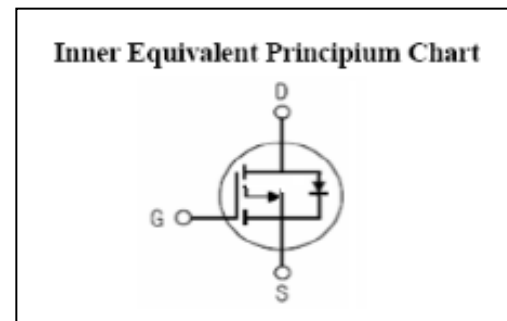
### Features :

- Fast Switching
- Low Gate Charge and Rds(on)
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test



### Applications :

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



### Absolute ( $T_c=25^{\circ}\text{C}$ unless otherwise specified ) :

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	-60	V
$I_D$	Continuous Drain Current	-65	A
	Continuous Drain Current $T_c=100^{\circ}\text{C}$	-45	A
$I_{DM}^{a1}$	Pulsed Drain Current	-260	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$dv/dt^{a3}$	Peak Diode Recovery $dv/dt$	5.0	V/ns
$P_D$	Power Dissipation	130	W
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	175 , -55 to 175	$^{\circ}\text{C}$
$T_L$	Maximum Temperature for Soldering	300	$^{\circ}\text{C}$

**Electrical Characteristics ( Tc=25°C unless otherwise specified ) :**

<b>OFF Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-60	--	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Bvdss Temperature Coefficient	I <sub>D</sub> =-250uA, Reference 25°C	--	0.02	--	V/°C
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> =-60, V <sub>GS</sub> = 0V, T <sub>a</sub> =25°C	--	--	-1	μA
		V <sub>DS</sub> =-48V, V <sub>GS</sub> = 0V, T <sub>a</sub> =125°C	--	--	-250	
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> = +20V	--	--	1	μA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> = -20V	--	--	-1	μA

<b>ON Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20.0A	--	13	23	mΩ
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0	--	-3.0	V
Pulse width tp ≤ 380μs, δ ≤ 2%						

<b>Dynamic Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-20A	--	25	--	S
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V f=1.0MHz	--	5700	--	pF
C <sub>oss</sub>	Output Capacitance		--	470	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	220	--	

<b>Resistive Switching Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	R <sub>L</sub> =-1.5Ω, V <sub>DD</sub> =-30V V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω	--	20	--	ns
t <sub>r</sub>	Rise Time		--	20	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	50	--	
t <sub>f</sub>	Fall Time		--	30	--	
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =-30.0A, V <sub>DD</sub> =-20V V <sub>GS</sub> =-10V	--	70	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	16	--	
Q <sub>gd</sub>	Gate to Drain ( "Miller" ) Charge		--	17	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$I_S$	Continuous Source Current (Body Diode)		--	--	-65	A
$I_{SM}$	Maximum Pulsed Current (Body Diode)		--	--	-120	A
$V_{SD}$	Diode Forward Voltage	$I_S = -30A, V_{GS} = 0V$	--	--	1.5	V
$t_{rr}$	Reverse Recovery Time	$I_S = -20A, T_j = 25^\circ C$	--	50	--	ns
$Q_{rr}$	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s, V_{GS} = 0V$	--	70	--	nC
Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$						

Symbol	Parameter	Typ.	Units
$R_{\theta JA}$	Junction-to-Ambient	0.97	$^\circ C/W$

<sup>a1</sup> : Repetitive rating; pulse width limited by maximum junction temperature

<sup>a3</sup> :  $I_{SD} = -30A, di/dt \leq 100A/\mu s, V_{DD} \leq BV_{DS}, \text{Start } T_j = 25^\circ C$

### Typical Electrical and Thermal Characteristics

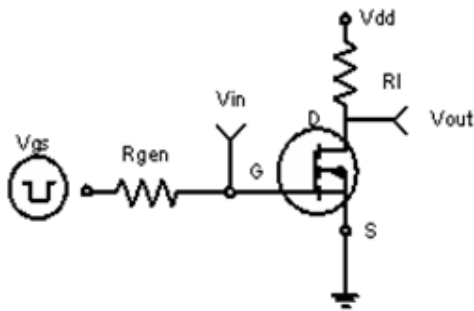


Figure 1: Switching Test Circuit

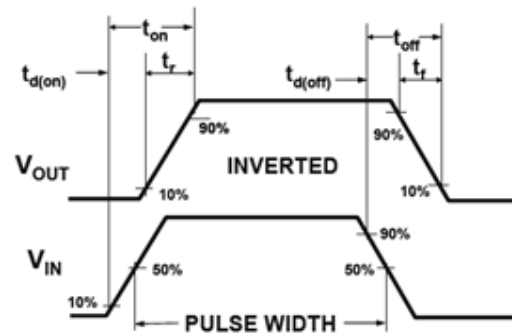
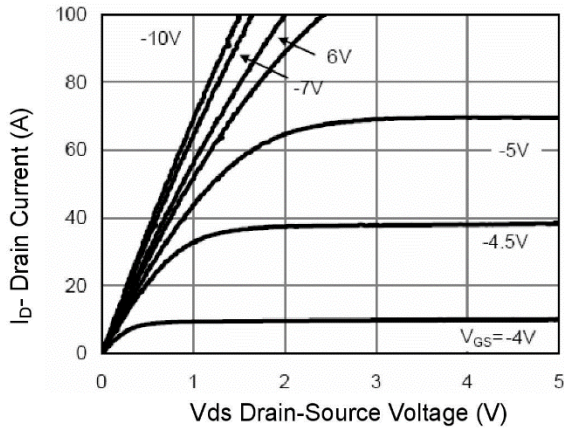
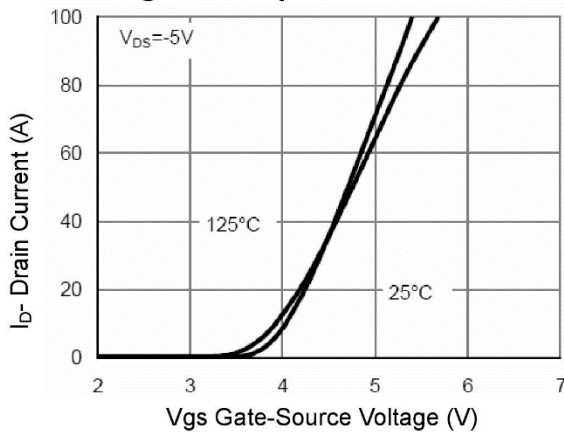


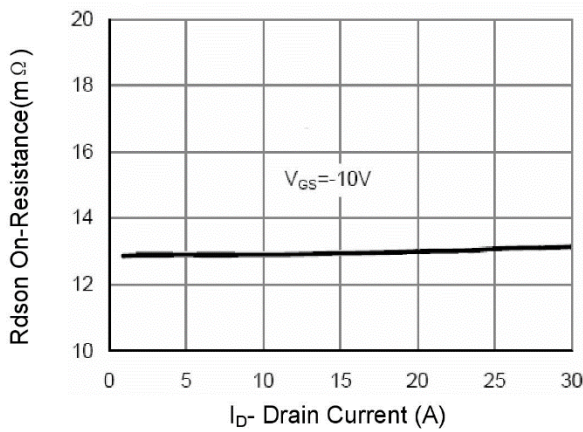
Figure 2: Switching Waveforms



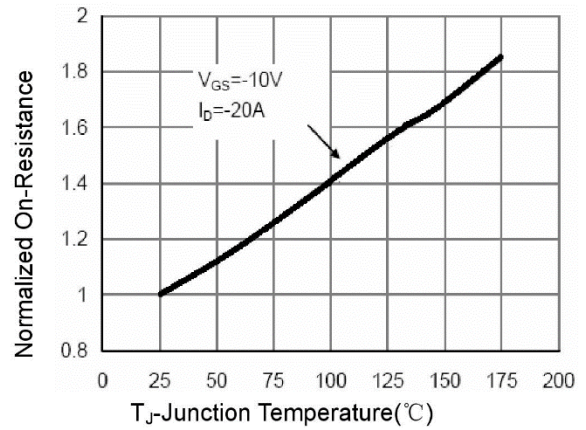
**Figure 1 Output Characteristics**



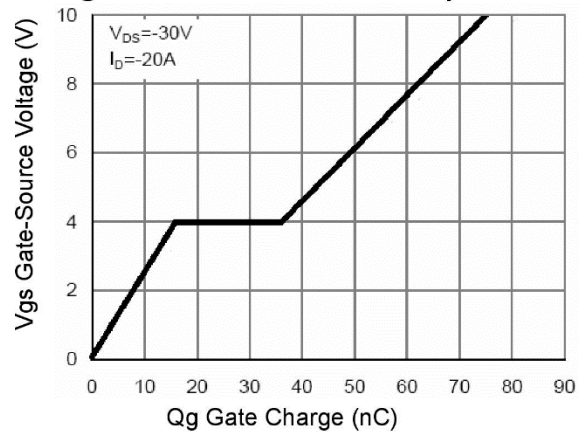
**Figure 2 Transfer Characteristics**



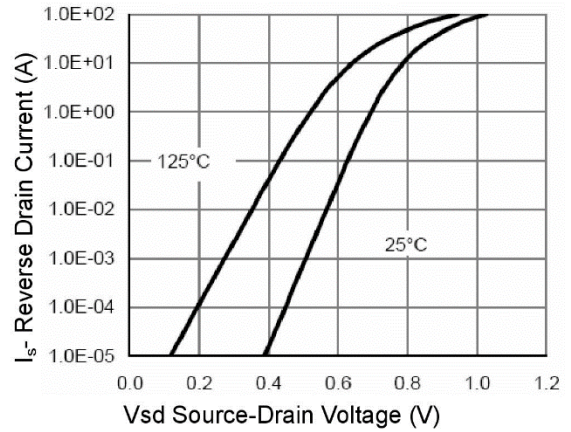
**Figure 3 Rds(on)- Drain Current**



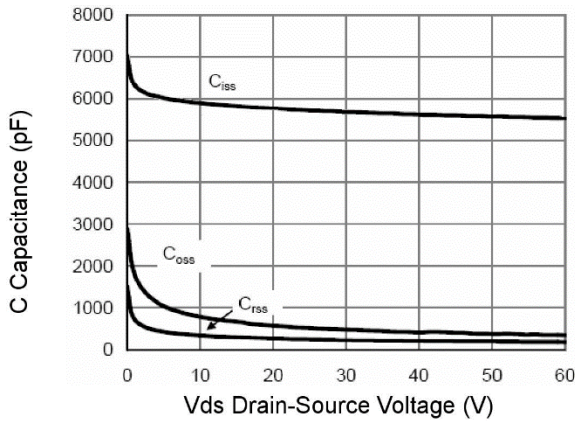
**Figure 4 Rds(on)-Junction Temperature**



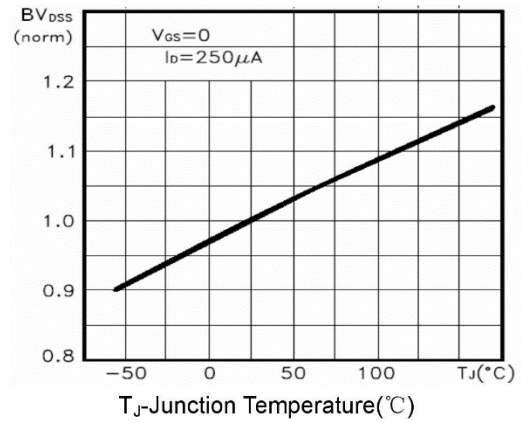
**Figure 5 Gate Charge**



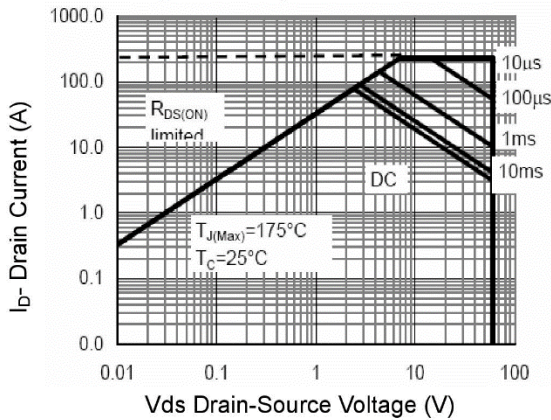
**Figure 6 Source- Drain Diode Forward**



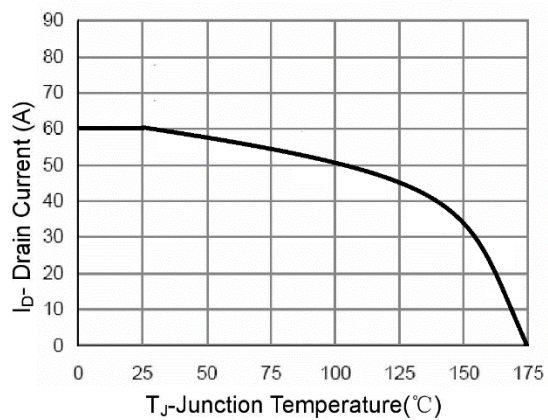
**Figure 7 Capacitance vs Vds**



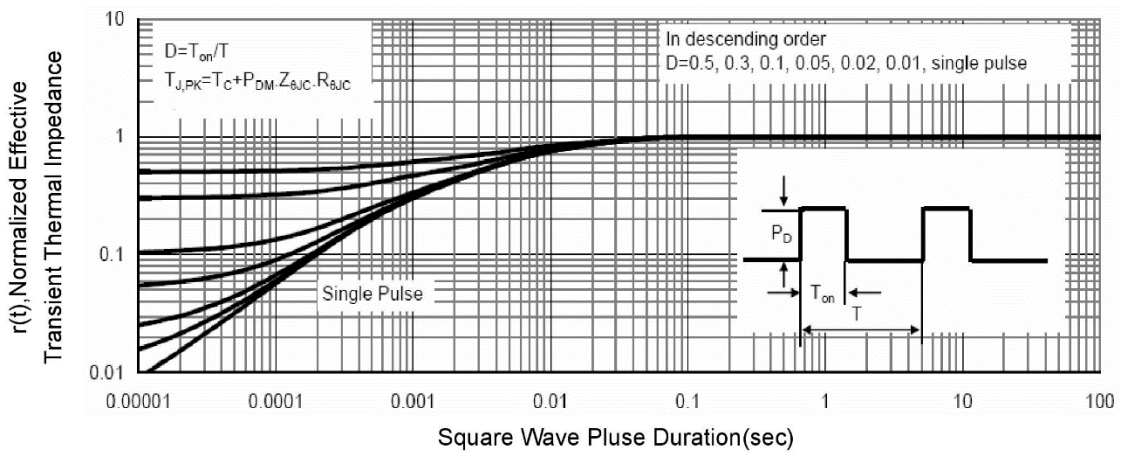
**Figure 9  $BV_{DSS}$  vs Junction Temperature**



**Figure 8 Safe Operation Area**



**Figure 10  $I_D$  Current Derating vs Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**