

650V Silicon Carbide Schottky Diode

$V_{RRM} = 650\text{ V}$
$I_F (T_c=150^\circ\text{C}) = 10\text{ A}$
$Q_c = 28\text{ nC}$

Features

- 650Volt Schottky Rectifier
- Shorter recovery time
- Highspeed switching possible
- HighFrequency Operation
- TemperatureIndependent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on VF

Applications

- HVAC
- Switch Mode Power Supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter Stages
- AC/DC converters

Benefits

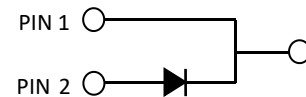
- Higher safety margin against overvoltage
- Improved efficiency all load conditions
- Increased efficiency compared to Silicon Diode alternatives
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses

Package

Type : TO-220I-2L insulated



1, Cathode 2, Anode



Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	650	V		
V_{RSM}	Surge Peak Reverse Voltage	650	V		
V_{DC}	DC Blocking Voltage	650	V		
I_F	Continuous Forward Current	10	A	$T_c=150^\circ\text{C}$	Fig. 7
I_{FRM}	Repetitive Peak Forward Surge Current	80	A	$T_c=25^\circ\text{C}$, $t_p=10\text{ ms}$, Half Sine Wave,	
I_{FSM}	Non-Repetitive Peak Forward Surge Current	105	A	$T_c=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Wave	
$I_{F,Max}$	Non-Repetitive Peak Forward Surge Current	840	A	$T_c=25^\circ\text{C}$, $t_p= 10\ \mu\text{s}$, Pulse	
P_{tot}	Power Dissipation	138 60	W	$T_c=25^\circ\text{C}$ $T_c=110^\circ\text{C}$	Fig. 6
T_j, T_{stg}	Operating Junction and Storage Temperature	-55 to +175	$^\circ\text{C}$		

Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_F	Forward Voltage	1.45 1.75	1.70 2.00	V	$I_F = 10\text{ A } T_J = 25^\circ\text{C}$ $I_F = 10\text{ A } T_J = 175^\circ\text{C}$	Fig. 1
I_R	Reverse Current	2 40	20 200	μA	$V_R = 650\text{ V } T_J = 25^\circ\text{C}$ $V_R = 650\text{ V } T_J = 175^\circ\text{C}$	Fig. 2
Q_C	Total Capacitive Charge	28		nC	$V_R = 400\text{ V}, I_F = 10\text{ A}, T_J = 25^\circ\text{C}$ $Q_C = \int_0^{V_R} C(V)dV$	Fig. 4
C	Total Capacitance	550 53 48		pF	$V_R = 0\text{ V}, T_J = 25^\circ\text{C}, f = 1\text{ MHz}$ $V_R = 200\text{ V}, T_J = 25^\circ\text{C}, f = 1\text{ MHz}$ $V_R = 400\text{ V}, T_J = 25^\circ\text{C}, f = 1\text{ MHz}$	Fig. 3
E_C	Capacitance Stored Energy	7.0		μJ	$V_R = 400\text{ V}$	Fig. 5

Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.83	$^\circ\text{C/W}$	Fig. 8

Typical Performance

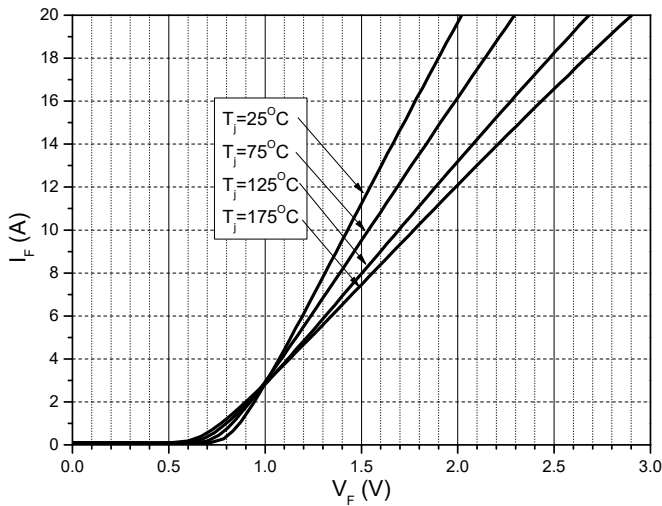


Figure 1. Forward Characteristics

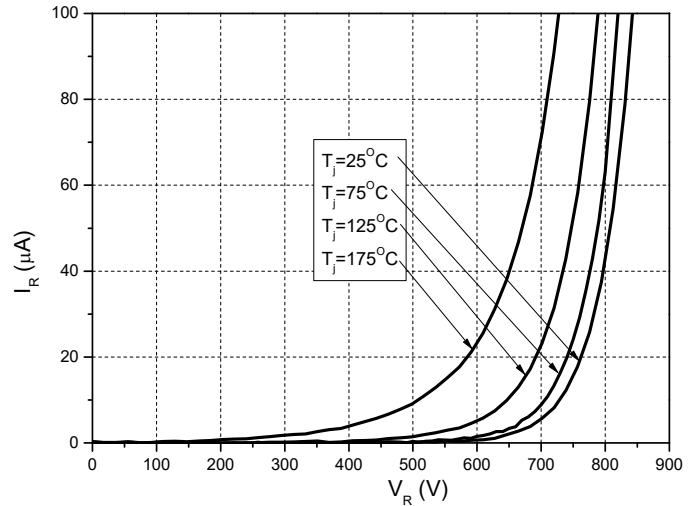


Figure 2. Reverse Characteristics

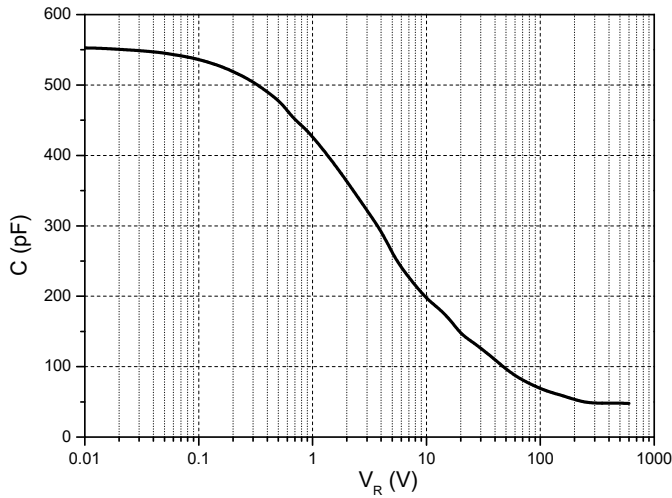


Figure 3. Capacitance vs. Reverse Voltage

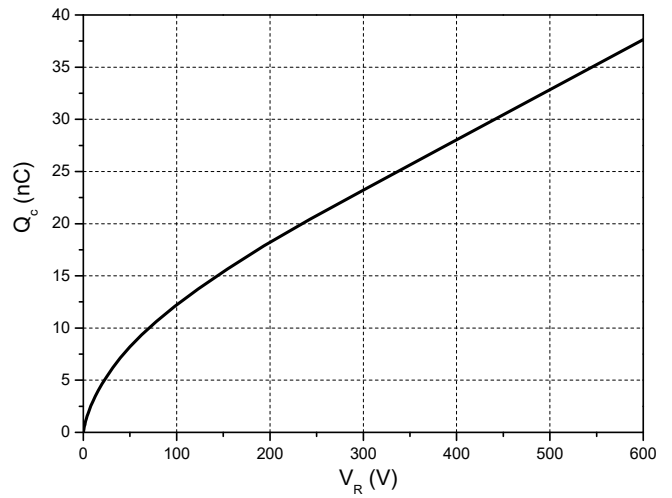


Figure 4. Total Capacitance Charge vs. Reverse Voltage

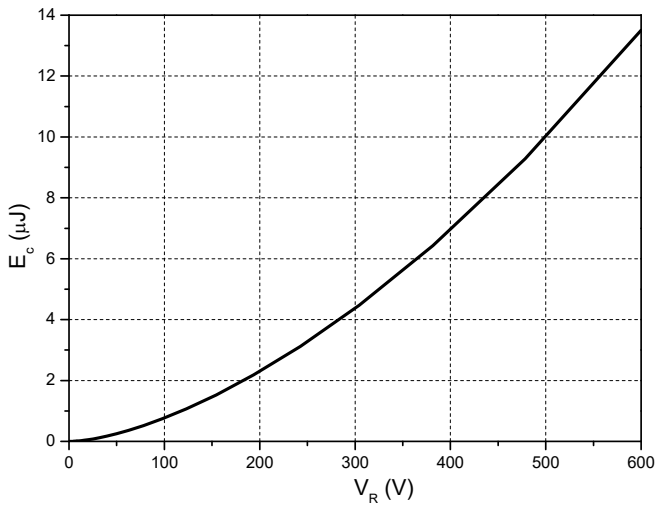


Figure 5. Capacitance Stored Energy

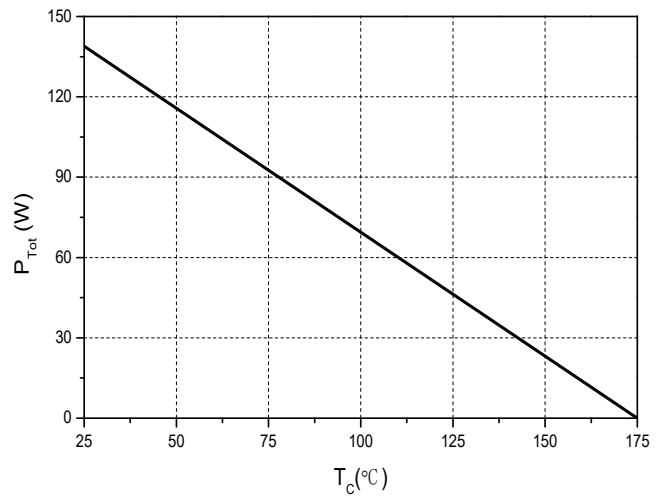


Figure 6. Power Derating

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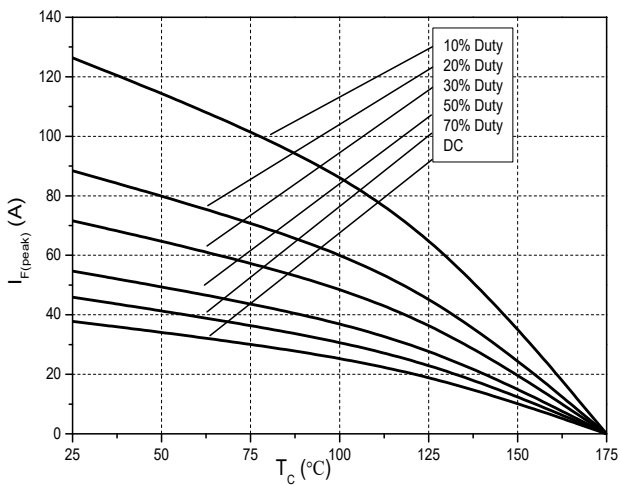


Figure 7. Current Derating

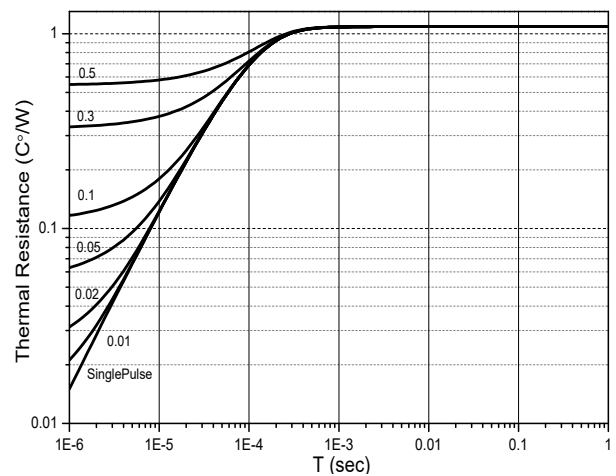
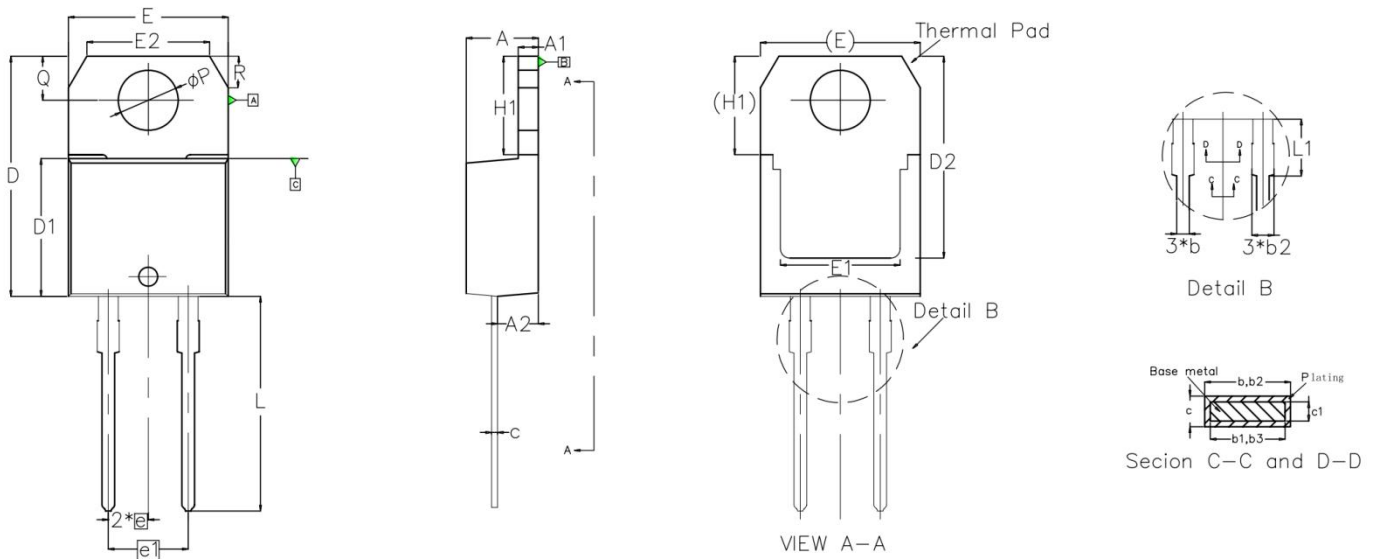


Figure 8. Transient Thermal Impedance

Package Dimensions: TO-220I-2L insulated



SYMBOL	MILLIMETERS			NOTES	SYMBOL	MILLIMETERS			NOTES
	Normal	MIN.	MAX.			Normal	MIN.	MAX.	
A	4.45	4.35	4.75		E1	7.60	6.86	8.89	
A1	1.27	1.14	1.40		e	2.54	2.41	2.67	
A2	2.60	2.40	2.80		e1	5.08	4.88	5.28	
b	0.85	0.69	1.01		H1	6.20	6.00	6.40	
b1	0.83	0.38	0.97		L	13.35	13.00	13.70	
b2	1.46	1.20	1.73		L1	3.00	2.70	3.30	
b3	1.44	1.14	1.73		ϕP	3.80	3.70	3.95	
c	0.50	0.36	0.61		Q	2.80	2.60	3.00	
c1	0.48	0.36	0.56		E2	7.80	7.50	8.10	
D	15.50	15.20	15.80		R	2.00	1.70	2.20	
D1		8.50	9.02						
D2	12.85	12.20	12.88						
E	10.18	10.00	10.40						